## IN THE SPECIFICATION

Please add the following Summary beginning on page 2, line 26:

## Summary

A first program unit is received in a parallel computing environment having a team of parallel threads including at least a first and second thread. The first program unit includes a memory copy operation to be performed between the first and second threads. The first program unit is translated into a second program unit. The second program unit associates the memory copy operation with a set of one or more instructions. The set of instructions ensures that the second thread copy's database, in part, is on a first descriptor associated with the first thread. An address of the first descriptor is copied to a two address buffer.